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Bib Data Sheet

CONFIRMATION NO. 4942

|                             |                                       |              |                        |                                   |
|-----------------------------|---------------------------------------|--------------|------------------------|-----------------------------------|
| SERIAL NUMBER<br>10/632,928 | FILING DATE<br>08/04/2003<br><br>RULE | CLASS<br>714 | GROUP ART UNIT<br>2138 | ATTORNEY DOCKET NO.<br>009683-476 |
|-----------------------------|---------------------------------------|--------------|------------------------|-----------------------------------|

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## \*\* CONTINUING DATA \*\*\*\*\*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003-073951(P) 03/18/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/30/2003

|                                 |   |          |         |        |             |
|---------------------------------|---|----------|---------|--------|-------------|
| Foreign Priority claimed        | <input type="checkbox"/> yes <input type="checkbox"/> no  | STATE OR | SHEETS  | TOTAL  | INDEPENDENT |
| 35 USC 119 (a-d) conditions met | <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | COUNTRY  | DRAWING | CLAIMS | CLAIMS      |
| Verified and Acknowledged       | Examiner's Signature _____ Initials _____   | JAPAN    | 9       | 9      | 1           |

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## TITLE

Semiconductor integrated circuit capable of testing with small scale circuit configuration

|                                   |   |   |
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